

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

EXPRESS MAIL NO. EV 327889053 US

Applicant : Davide Tonietto, et al.  
Application No. : Not Assigned  
Filed : Herewith  
Title : INTEGRATED DECISION FEEDBACK EQUALIZER AND CLOCK  
AND DATA RECOVERY

Grp./Div. : Not Assigned  
Examiner : Not Assigned

Docket No. : 51715/SDB/B600

INFORMATION DISCLOSURE STATEMENT  
37 CFR § 1.97(b)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

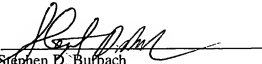
PostOffice Box 7068  
Pasadena, CA 91109-7068  
April 13, 2004

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing references that is known to applicant. Copies of the listed references are enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,  
CHRISTIE, PARKER & HALE, LLP

By   
Stephen D. Burbach  
Reg. No. 40,285  
626/795-9900

SDB/cah  
Enclosures: Form PTO/SB/08A/B, w/reference  
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| FORM PTO/SB/08A/B (10-01)<br>Substitute for PTO-1449A/B   | <b>Attorney Docket Number</b> | <b>51715/SDB/B600</b>        |
| <b>INFORMATION DISCLOSURE</b><br><br><b>STATEMENT BY APPLICANT</b><br><br>(use as many sheets as necessary) | <b>Application Number</b>     | Not Assigned 10/823252       |
|   | <b>Filing Date</b>            | Herewith 4/13/2004           |
|   | <b>Applicant(s)</b>           | Davide Tonietto, et al.      |
|   | <b>Group Art Unit</b>         | Not Assigned 2611            |
|   | <b>Examiner Name</b>          | Not Assigned /Rahel Guarino/ |

| U.S. PATENT DOCUMENTS |                       |   |                                |                  |
|-----------------------|-----------------------|---|--------------------------------|------------------|
| EXAMINER INITIALS     | Cite No. <sup>1</sup> | DOCUMENT NUMBER<br>Number - Kind Code <sup>2</sup> (If Known) | Publication Date<br>MM-DD-YYYY | Name of Patentee |
| /RG/                  |                       | 6,340,899 B1  | 01-22-02002                    | Green            |
| /RG/                  |                       | 2001/0033407 A1   | 10-25-2001                     | Cao              |

| FOREIGN PATENT DOCUMENTS |                       |   |                                |  |                       |
|--------------------------|-----------------------|---|--------------------------------|--|-----------------------|
| EXAMINER INITIALS        | Cite No. <sup>1</sup> | FOREIGN PATENT DOCUMENT<br>Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup><br>(If Known) | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | T <sup>6</sup><br>(✓) |
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| OTHER DOCUMENTS   |                       |  |
|-------------------|-----------------------|--|
| EXAMINER INITIALS | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. |
| /RG/              |                       | THOMAS E. LEE, et al., "A 155-MHz Clock Recovery Delay- and Phase-Locked Loop," <i>IEEE Journal of Solid-State Circuits</i> , December 1992, pp. 1736-1746, vol. SC-27.  |
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|---|-----------------|-----------------|------------|
| EXAMINER SIGNATURE  | /Rahel Guarino/ | DATE CONSIDERED | 08/05/2008 |
| EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at www.pto.gov or MPEP 901.4. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English Language Translation is attached. |                 |                 |            |